FIG. 1

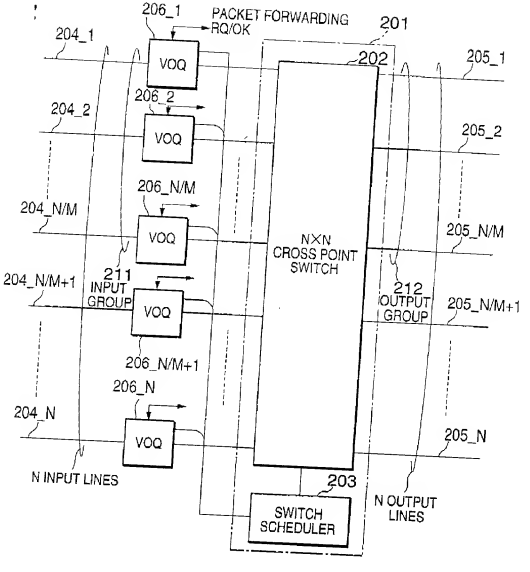

206_1
PACKET FORWARDING RQ/OK
201
204_1
VOQ
202
205_1
206_2
204_2
VOQ
205_2
206_N/M
204_N/M
VOQ
N×N CROSS POINT SWITCH
205_N/M
204_N/M+1
211 INPUT GROUP
VOQ
212 OUTPUT GROUP
205_N/M+1
206_N/M+1
206_N
204_N
VOQ
205_N
N INPUT LINES
203
SWITCH SCHEDULER
N OUTPUT LINES

FIG. 2

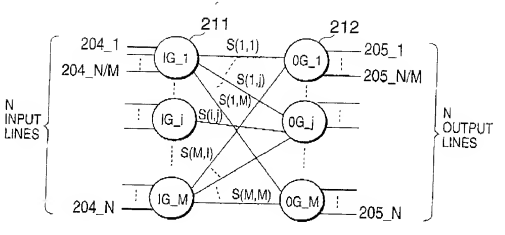

211
212
204_1
204_N/M
IG_1
IG_j
IG_M
S(1,1)
S(1,j)
S(1,M)
S(i,j)
S(M,I)
S(M,M)
0G_1
0G_j
0G_M
205_1
205_N/M
205_N
N
INPUT
LINES
N
OUTPUT
LINES

FIG. 3

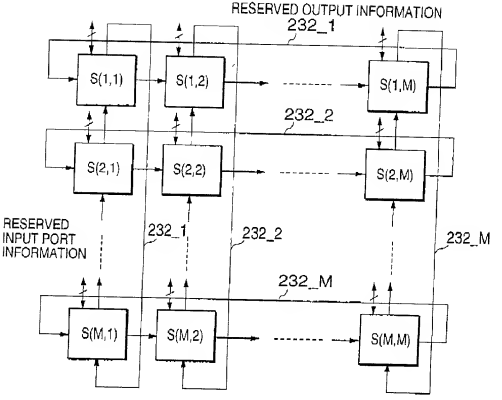
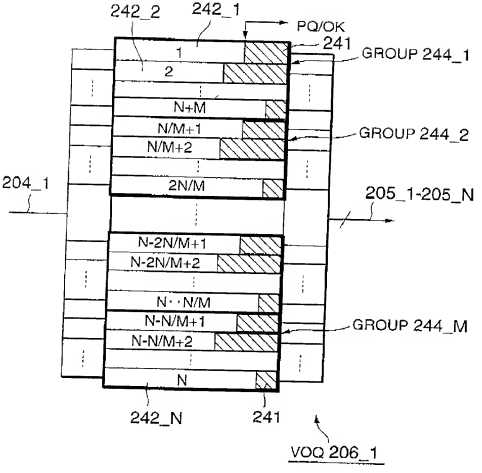

RESERVED OUTPUT INFORMATION
232_1
S(1,1)
S(1,2)
S(1,M)
232_2
S(2,1)
S(2,2)
S(2,M)
RESERVED
INPUT PORT
INFORMATION
232_1
232_2
232_M
232_M
S(M,1)
S(M,2)
S(M,M)

FIG. 4



1010021-6699660

FIG. 5A

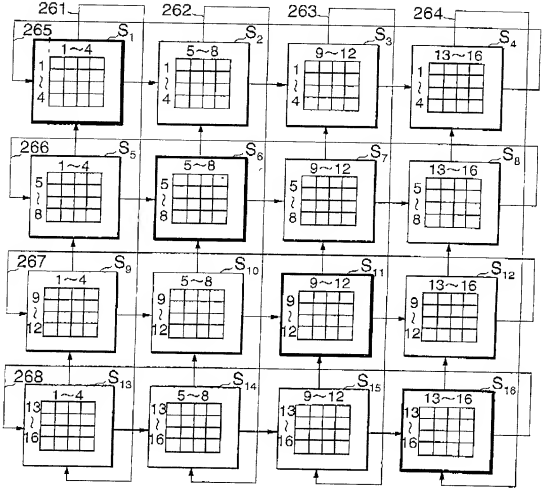
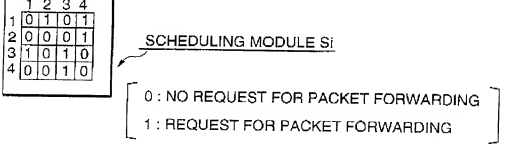
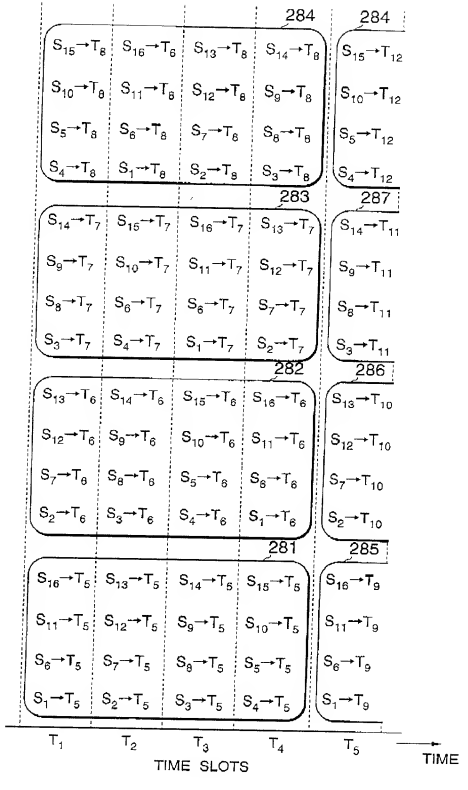


FIG. 5B



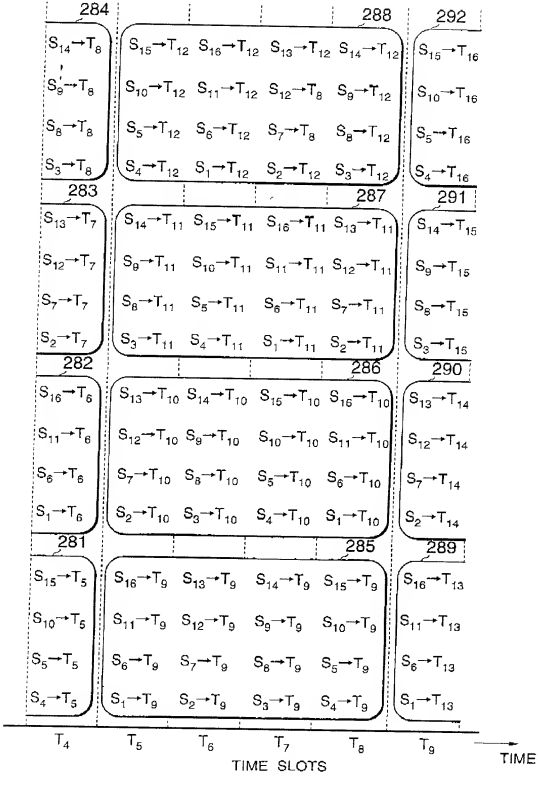
FUJITSU 5699660

FIG. 6



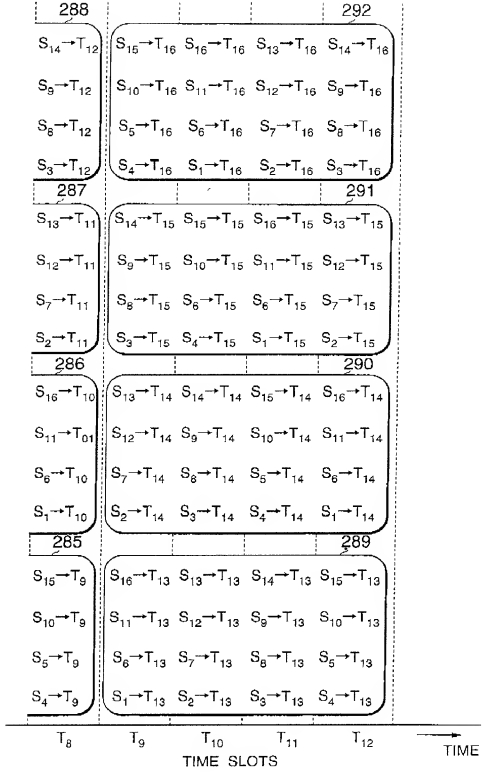
I0110D-66999660

FIG. 7



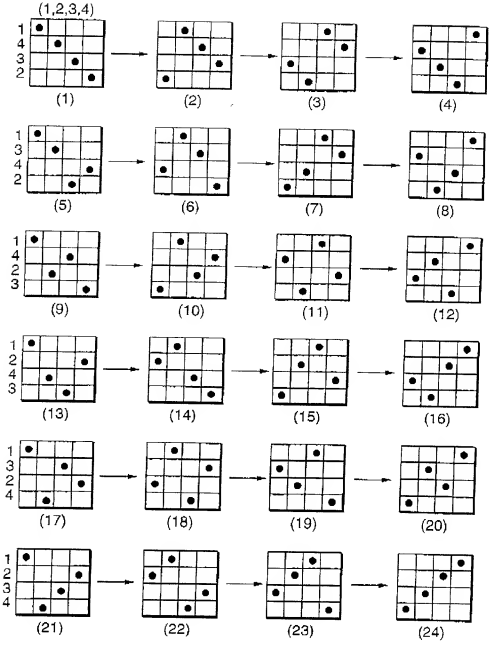
EP 0 609 960 A1

FIG. 8



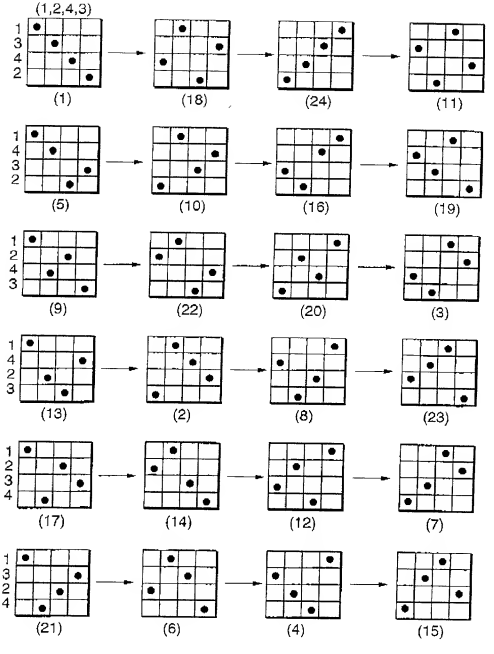
EP 0 609 660 A1

FIG. 9



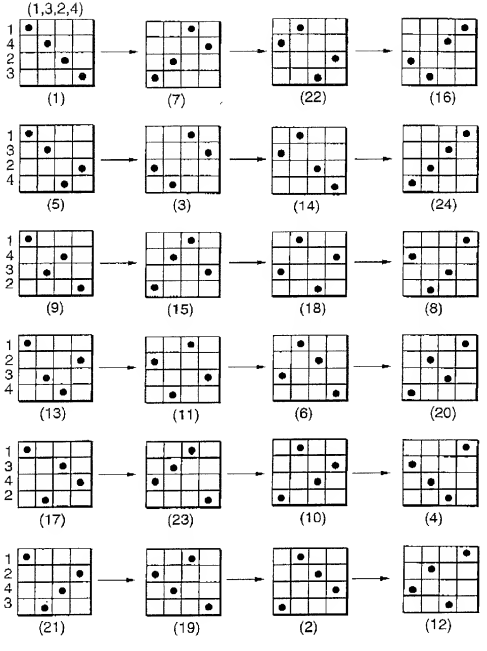
TOTOOF "6609663

FIG. 10



TOKYO 6059660

FIG. 11



EPO/EPA 6099660

FIG. 12

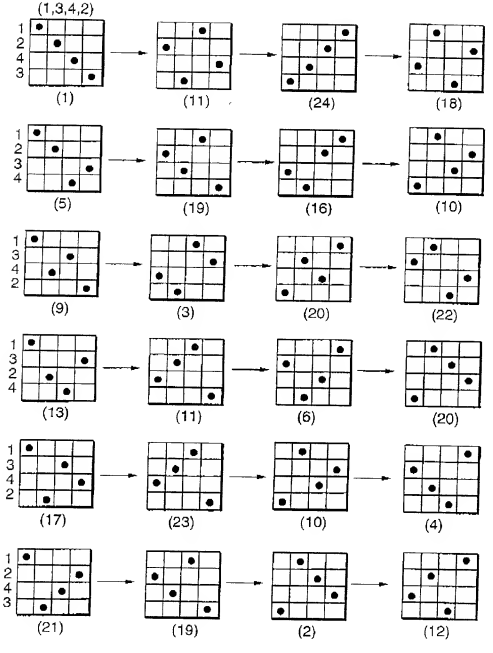

(1,3,4,2)
1
2
4
3
(1)
(11)
(24)
(18)
1
2
3
4
(5)
(19)
(16)
(10)
1
3
4
2
(9)
(3)
(20)
(22)
1
3
2
4
(13)
(11)
(6)
(20)
1
3
4
2
(17)
(23)
(10)
(4)
1
2
4
3
(21)
(19)
(2)
(12)

FIG. 13

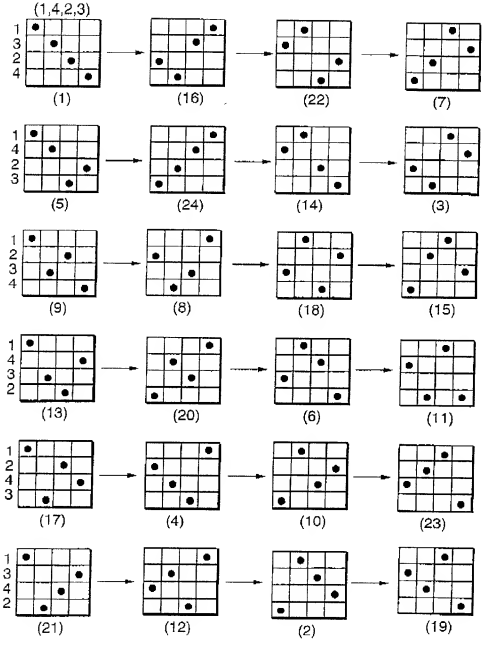


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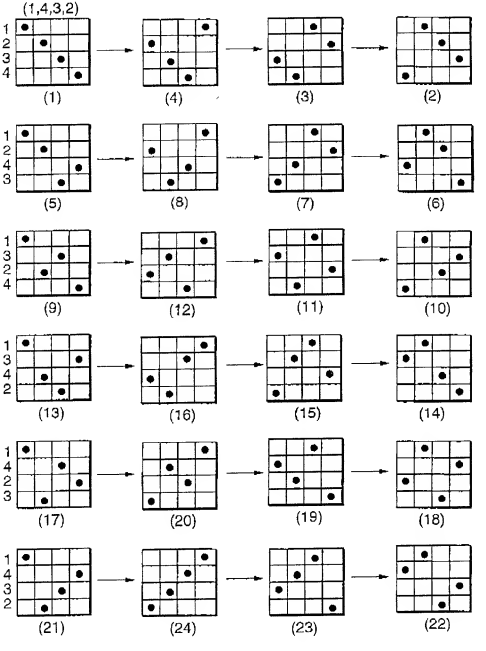
FIG. 14



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